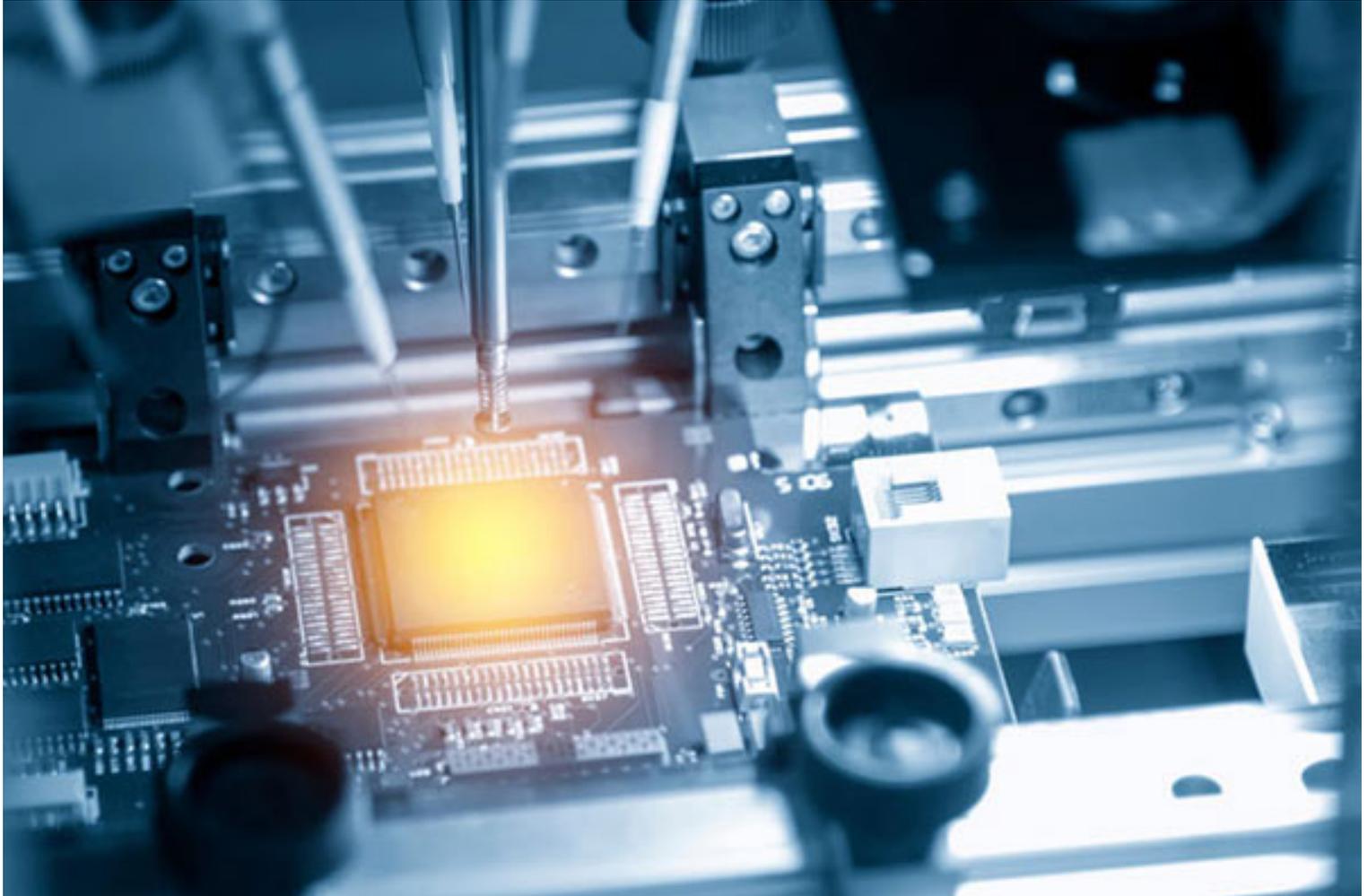


## Applications invited under the Design Linked Incentive (DLI) Scheme

By IASToppers | 2022-01-18 17:15:00



### Applications invited under the Design Linked Incentive (DLI) Scheme

Recently, the Union Ministry of Electronics and Information (MeitY) invited applications from 100 domestic companies, start-ups, and MSMEs under its Design Linked Incentive (DLI) Scheme.



आज़ादी का  
अमृत महोत्सव



संघीय लोकतान्त्रिक गणतन्त्र  
भारत  
Ministry of Electronics & Information Technology (MeitY)  
Government of India



Digital India  
Power To Empower



## Design Linked Incentive Scheme

**Design chips in India, for the world !**



Online applications invited by C-DAC from  
**01st January 2022 to 31st December 2024** on DLI Scheme portal  
at [www.chips-dli.gov.in](http://www.chips-dli.gov.in)

### Objectives of the Scheme:

The Design Linked Incentive (DLI) Scheme aims to offer financial incentives as well as design infrastructure support across various stages of development and deployment of semiconductor design for Integrated Circuits (ICs), Chipsets, System on Chips (SoCs), Systems & IP Cores and semiconductor linked design over a period of 5 years with overall objectives of:

- (i) Nurturing domestic companies of semiconductor design for Integrated Circuits (ICs), Chipsets, System on Chips (SoCs), Systems & IP Cores and semiconductor linked design and facilitating their growth.
- (ii) Achieving significant indigenization in semiconductor content and IPs involved in the electronic products deployed in the country, thereby facilitating import substitution and value addition in electronics sector.
- (iii) Strengthening the design infrastructure for semiconductor design and facilitating access to startups and MSMEs.

### Fiscal support under the DLI Scheme:

- P-DLI ( Product Design Linked Incentive): 50% of reimbursement subject to a ceiling of Rs. 15 Crore for design activities
- DLI (Deployment Linked Incentive) : 04-06% of reimbursement subject to a ceiling of Rs. 30 Crore on sales of approved semiconductor goods
- Chip Design infrastructure support viz. National EDA Tool Grid, IP Core repository, MPW prototyping and post-silicon validation activities

### Beneficiaries

**100 Domestic companies, Startups and MSMEs**

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[ref:Twitter]



# DESIGN CHIPS IN INDIA, **FOR THE WORLD!**

## Design Linked Incentive Scheme (DLI)

Fiscal support includes:

**50%** of reimbursement subject to a ceiling of Rs. 15 Crore for design activities

**4-6%** of reimbursement subject to a ceiling of Rs. 30 Crore on sales of approved goods

Chip Design Infrastructure Support

#AatmanirbharInChipDesign



[ref:Twitter]

### Design Linked Incentive (DLI) Scheme:

- **Launched by:** Ministry of Electronics and Information (MeitY) in 2021.
- **Implemented by:** **C-DAC (Centre for Development of Advanced Computing)**, under **MeitY**.

- **Objective:** To reduce the problems of the domestic industry involved in semiconductor design to strengthen the **semiconductor chip design ecosystem** in the country.
- **Beneficiaries:**
  - Indian companies (Private Limited Company or a Public Limited Company)
    - It includes Indian MSMEs and Startups.

### Component:

Under the DLI scheme, Approved companies can claim their expenditure/incentive in 3 schemes of DLI:

- **Design Infrastructure support:** Reimbursement of up to ₹30 Lakh per application for fabrication of design and post-silicon validation activities.
- **Product Design Linked Incentive (P-DLI):** Milestones linked reimbursement of up to 50% of the eligible expenditure subject to a ceiling of ₹15 Crore per application.
  - It is given for designing semiconductor goods and demonstrating them in the operational environment and volume of production.
- **Deployment Linked Incentive (DLI):** Reimbursement of 6-4% of net sales of designed semiconductor goods over 5 years subject to a ceiling of ₹30 Crore.